

JAN 03 2003

PATENT & TRADEMARK OFFICE

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Noriaki Oda, et al.

Examiner: Monica Lewis

Serial No: 09/863,737

Art Unit: 2822

Filed: May 23, 2001

Docket: 12562A

For: SEMICONDUCTOR DEVICE AND
METHOD FOR MANUFACTURING
THE SAME

Dated: December 30, 2002

Assistant Commissioner for Patents
Washington, D.C. 20231

107
Amend 3
1/14/03
JLBN

AMENDMENT AND RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action dated August 28, 2002, applicants submit
the following Amendment for entry in the above-identified case.

IN THE CLAIMS:

Please cancel Claims 2 and 6 without prejudice.

Please amend Claims 1, 3, 5 and 7 as follows:

Subj C.1
1. (Amended) A semiconductor device having a plurality of wirings
juxtaposed with one another and a SiOF insulating film being in contact with the wirings,

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States
Postal Service as first class mail in an envelope addressed to: Assistant Commissioner
for Patents, Washington, D.C. 20231 on December 30, 2002.

Dated: December 30, 2002

Janet Grossman
Janet Grossman